

LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

APR 25 2002

Applicant(s): A.K. Iyengar et al.
Docket No.: YOR92010661US1
Serial No.: 10/092,766
Filing Date: March 7, 2002
Group: ~~2185~~ 2186COPY OF PAPERS
ORIGINALLY FILED

U.S. PATENT DOCUMENTS

EXAMINER						FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS		IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER						TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS		YES NO

OTHER DOCUMENTS

EXAMINER				
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.		

- BJP 1. "Computer Architecture: A Quantitative Approach" by J. Hennessy and D. Patterson, 2nd ed., 1996.
- BJP 2. P. Rodriguez, C. Spanner and E. Biersack, "Web Caching Architectures: Hierarchical and Distributed Caching," Proceedings of the 4th International Web Caching Workshop, March 1999.
- BJP 3. "Improving Performance with Application-Level Caching" by L. Degenaro, A. Iyengar, and I. Rouvellou, Proceedings of SSGRR 2001.

RECEIVED

APR 29 2002

Technology Center 2100

Examiner



Date Considered

3/8/04

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.